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**New Loader Eromupgrade 3329 D ##HOT##**

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by 1, 2020 New loader for 3329d (all data type stays the same) by dsaidadddaaddda. See attachment 1 1 By 1, 2020 A: Encapsulate this code in a file named NewLoader.txt. Load the file into a tool called Loader. Drop your existing loader. Load the new loader. Done. You'll need a serial device and a little bit of patience to get it configured, but once you do, you should be able to use this tool to keep your system up to date.

1. Field of the Invention The present invention relates to a method of manufacturing an object to be subjected to photolithography, a method of manufacturing a device using the object, an electronic apparatus, and an element substrate for an electronic apparatus.

2. Description of the Related Art Semiconductor devices typified by a semiconductor integrated circuit and a display device typified by a liquid crystal display device are fabricated by a process that forms thin films on a substrate to be subjected to photolithography, etching, and the like. A variety of techniques are used in the photolithography, etching, and the like. A projection exposure apparatus is an example of a system of transferring a pattern of a photo mask (hereinafter, referred to as a "reticle") onto a substrate (hereinafter, referred to as a "wafer"). The photolithography is performed by using a projection optical system to project the pattern on the photo mask (reticle) onto a plurality of shot areas on the substrate (wafer). A liquid immersion method is known as a method of improving the resolution. The liquid immersion method is disclosed in, for example, U.S. Pat. Nos. 5,515,540 and 6,212,922, and Japanese Patent Laid-Open No. 7-220990. A projection exposure apparatus that uses a liquid immersion method is also proposed (see U.S. Pat. No. 5,691,831). The liquid immersion method is described below. FIG. 10 is a schematic cross-sectional view of a projection exposure apparatus that performs a pattern transfer using the liquid immersion method. The projection exposure apparatus shown in FIG. 10 is so adapted as to form a circuit pattern on a substrate (wafer) W. A photo mask R having a circuit pattern

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